

Test Booklet Code & Serial No.

प्रश्नपत्रिका कोड व क्रमांक

Paper-II**ELECTRONIC SCIENCE****A****Signature and Name of Invigilator**

Seat No.

1. (Signature)

(In figures as in Admit Card)

(Name)

Seat No.

2. (Signature)

(In words)

(Name)

OMR Sheet No.

APR - 38224**Time Allowed : 2 Hours]**

(To be filled by the Candidate)

[Maximum Marks : 200**Number of Pages in this Booklet : 24****Number of Questions in this Booklet : 100****Instructions for the Candidates**

- Write your Seat No. and OMR Sheet No. in the space provided on the top of this page.
- This paper consists of **100** objective type questions. Each question will carry *two* marks. *All* questions of Paper II will be compulsory. At the commencement of examination, the question booklet will be given to the student. In the first 5 minutes, you are requested to open the booklet and compulsorily examine it as follows :
 - To have access to the Question Booklet, tear off the paper seal on the edge of this cover page. Do not accept a booklet without sticker-seal or open booklet.
 - Tally the number of pages and number of questions in the booklet with the information printed on the cover page. Faulty booklets due to missing pages/questions or questions repeated or not in serial order or any other discrepancy should not be accepted and correct booklet should be obtained from the invigilator within the period of 5 minutes. Afterwards, neither the Question Booklet will be replaced nor any extra time will be given. The same may please be noted.
 - After this verification is over, the OMR Sheet Number should be entered on this Test Booklet.
- Each question has four alternative responses marked (A), (B), (C) and (D). You have to darken the circle as indicated below on the correct response against each item.
Example : where (C) is the correct response.

(A)

(B)

(C)

(D)
- Your responses to the items are to be indicated in the **OMR Sheet given inside the Booklet only**. If you mark at any place other than in the circle in the OMR Sheet, it will not be evaluated.
- Read instructions given inside carefully.
- Rough Work is to be done at the end of this booklet.
- If you write your Name, Seat Number, Phone Number or put any mark on any part of the OMR Sheet, except for the space allotted for the relevant entries, which may disclose your identity, or use abusive language or employ any other unfair means, you will render yourself liable to disqualification.
- You have to return original OMR Sheet to the invigilator at the end of the examination compulsorily and must not carry it with you outside the Examination Hall. You are, however, allowed to carry the Test Booklet and duplicate copy of OMR Sheet on conclusion of examination.
- Use only Blue/Black Ball point pen.
- Use of any calculator or log table, etc., is prohibited.
- There is no negative marking for incorrect answers.

विद्यार्थ्यासाठी महत्वाच्या सूचना

- परिक्षार्थींनी आपला आसन क्रमांक या पृष्ठावरील वरच्या कोपऱ्यात लिहावा. तसेच आपणांस दिलेल्या उत्तरपत्रिकेचा क्रमांक त्याखाली लिहावा.
- सदर प्रश्नपत्रिकेत **100** बहुपर्यायी प्रश्न आहेत. प्रत्येक प्रश्नास **दोन** गुण आहेत. या प्रश्नपत्रिकेतील **सर्व** प्रश्न सोडविणे अनिवार्य आहे.
- परीक्षा सुरू झाल्यावर विद्यार्थ्यांना प्रश्नपत्रिका दिली जाईल. सुरुवातीच्या 5 मिनीटांमध्ये आपण सदर प्रश्नपत्रिका उघडून खालील बाबी अवश्य तपासून पहाव्यात.
 - प्रश्नपत्रिका उघडण्यासाठी प्रश्नपत्रिकेवर लावलेले सील उघडावे. सील नसलेली किंवा सील उघडलेली प्रश्नपत्रिका स्विकारू नये.
 - पहिल्या पृष्ठावर नमूद केल्याप्रमाणे प्रश्नपत्रिकेची एकूण पृष्ठे तसेच प्रश्नपत्रिकेतील एकूण प्रश्नांची संख्या पडताळून पहावी. पृष्ठे कमी असलेली/कमी प्रश्न असलेली/प्रश्नांचा चुकीचा क्रम असलेली किंवा इतर त्रुटी असलेली सदोष प्रश्नपत्रिका सुरुवातीच्या 5 मिनिटातच पर्यवेक्षकाला परत देऊन दूसरी प्रश्नपत्रिका मागवून घ्यावी. त्यानंतर प्रश्नपत्रिका बदलून मिळणार नाही तसेच वेळही वाढवून मिळणार नाही याची कृपया विद्यार्थ्यांनी नोंद घ्यावी.
 - वरीलप्रमाणे सर्व पडताळून पाहिल्यानंतरच प्रश्नपत्रिकेवर ओ.एम.आर. उत्तरपत्रिकेचा नंबर लिहावा.
- प्रत्येक प्रश्नासाठी (A), (B), (C) आणि (D) अशी चार विकल्प उत्तरे दिली आहेत. त्यातील योग्य उत्तराचा रकाना खाली दर्शविल्याप्रमाणे ठळकपणे काळा/निळा करावा.
उदा. : जर (C) हे योग्य उत्तर असेल तर.

(A)

(B)

(C)

(D)
- या प्रश्नपत्रिकेतील प्रश्नांची उत्तरे **ओ.एम.आर. उत्तरपत्रिकेतच दर्शवावीत**. इतर ठिकाणी लिहिलेली उत्तरे तपासली जाणार नाहीत.
- आत दिलेल्या सूचना काळजीपूर्वक वाचाव्यात.
- प्रश्नपत्रिकेच्या शेवटी जोडलेल्या कोन्या पानावरच कच्चे काम करावे.
- जर आपण ओ.एम.आर. वर नमूद केलेल्या ठिकाणा व्यतिरिक्त इतर कोठेही नाव, आसन क्रमांक, फोन नंबर किंवा ओळख पटेल अशी कोणतीही खूण केलेली आढळून आल्यास अथवा असभ्य भाषेचा वापर किंवा इतर गैरमार्गांचा अवलंब केल्यास विद्यार्थ्यांना परीक्षेस अपात्र ठरविण्यात येईल.
- परीक्षा संपल्यानंतर विद्यार्थ्यांनी मूळ ओ.एम.आर. उत्तरपत्रिका पर्यवेक्षकांकडे परत करणे आवश्यक आहे. तथापि, प्रश्नपत्रिका व ओ.एम.आर. उत्तरपत्रिकेची द्वितीय प्रत आपल्याबरोबर नेण्यास विद्यार्थ्यांना परवानगी आहे.
- फक्त निळा किंवा काळा बॉल पेनचाच वापर करावा.
- कॅलक्युलेटर किंवा लॉग टेबल वापरण्यास परवानगी नाही.
- चुकीच्या उत्तरासाठी गुण कपात केली जाणार नाही.

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Your Personal Exam Guide

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Electronic Science Paper II

Time Allowed : 120 Minutes]

[Maximum Marks : 200

Note : This Paper contains **Hundred (100)** multiple choice questions. Each question carrying **Two (2)** marks. Attempt *All* questions.

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| <p>1. What is the main purpose of a Schottky diode in semiconductor applications ?</p> <p>(A) Voltage regulation</p> <p>(B) High-speed switching</p> <p>(C) Temperature sensing</p> <p>(D) Power amplification</p> <p>2. What is the primary advantage of using a Gallium Nitride (GaN) semiconductor over traditional silicon in power electronics ?</p> <p>(A) Higher electron mobility</p> <p>(B) Lower bandgap</p> <p>(C) Greater thermal stability</p> <p>(D) Improved resistance to radiation</p> | <p>3. What type of backlighting is commonly used in LCD displays for improved color reproduction ?</p> <p>(A) Fluorescent backlighting</p> <p>(B) Incandescent backlighting</p> <p>(C) LASER backlighting</p> <p>(D) OLED backlighting</p> <p>4. What is the purpose of connecting a resistor in series with a Zener diode in a voltage regulator circuit ?</p> <p>(A) To increase the breakdown voltage</p> <p>(B) To limit the forward bias current</p> <p>(C) To decrease the reverse bias voltage</p> <p>(D) To provide mechanical stability</p> |
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| <p>5. Which type of low-dimensional semiconductor device is known for its ability to confine charge carriers in all three dimensions ?</p> <p>(A) Quantum wells</p> <p>(B) Quantum wires</p> <p>(C) Quantum dots</p> <p>(D) Quantum cascade devices</p> <p>6. What is the role of the series resistance in the I-V characteristics of a solar cell ?</p> <p>(A) To reduce the open-circuit voltage</p> <p>(B) To decrease the short-circuit current</p> <p>(C) To limit the current flow in the external circuit</p> <p>(D) To maximize the fill factor</p> | <p>7. What is the role of the “Dirac cones” in the electronic structure of graphene ?</p> <p>(A) They represent the valence and conduction bands</p> <p>(B) They indicate the presence of a bandgap</p> <p>(C) They show the dispersion relation of electrons</p> <p>(D) They enhance the magnetic properties of graphene</p> <p>8. How does the chirality of a carbon nanotube affect its electronic properties ?</p> <p>(A) It determines whether the nanotube is metallic or semiconducting</p> <p>(B) It affects the mechanical strength of the nanotube</p> <p>(C) It influences the thermal conductivity of the nanotube</p> <p>(D) It is unrelated to the electronic properties of nanotubes</p> |
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| <p>9. How does ZnO exhibit both semiconducting and piezoelectric properties simultaneously ?</p> <p>(A) Through the manipulation of doping concentration</p> <p>(B) Due to its wurtzite crystal structure</p> <p>(C) By controlling the synthesis temperature</p> <p>(D) It does not possess piezoelectric properties</p> | <p>11. Which of the following statements is true in case of CMOS fabrication ?</p> <p>(A) Fabrication of n-mos transistor requires few additional steps compared to p-MOS transistor</p> <p>(B) Fabrication on n-MOS is same as that of p-MOS transistor</p> <p>(C) Fabrication on n-MOS is different from that of p-MOS transistor</p> <p>(D) Fabrication of p-MOS transistor requires few additional steps compared to n-MOS transistor</p> |
| <p>10. In an LED, what is the function of the p-n junction ?</p> <p>(A) To modulate the intensity of light</p> <p>(B) To amplify the input signal</p> <p>(C) To emit light when forward-biased</p> <p>(D) To store electrical charge</p> | <p>12. EDAX is a technique used in conjunction with SEM for :</p> <p>(A) Energy dispersive analysis of X-rays</p> <p>(B) Energy levels occupied by electrons excited by X-rays</p> <p>(C) Electron diffraction analysis of X-ray excitations</p> <p>(D) Obtaining higher magnification levels</p> |

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| <p>13. The isolated active areas are created by technique known as</p> <p>(A) Selective deposition of field-oxide isolation</p> <p>(B) Local Oxidation of Silicon</p> <p>(C) Etched field-oxide isolation or Local Oxidation of Silicon</p> <p>(D) Reverse biased pn junction</p> <p>14. The gate delay is proportional to :</p> <p>(A) $R_{on}.C_g$</p> <p>(B) $R_s.C_d$</p> <p>(C) $R_d.C_g$</p> <p>(D) $R_{on}.C_{ox}$</p> <p>15. At threshold voltage, the surface potential is :</p> <p>(A) Negative Fermi potential</p> <p>(B) Fermi potential</p> <p>(C) 2 Fermi potential</p> <p>(D) -2 Fermi potential</p> | <p>16. What does CCD stand for ?</p> <p>(A) Control Circuit Design</p> <p>(B) Central Core Database</p> <p>(C) Charge-Coupled Device</p> <p>(D) Composite Camera Device</p> <p>17. As die size shrinks. the complexity of making the photomasks</p> <p>(A) increases</p> <p>(B) decreases</p> <p>(C) remains the same</p> <p>(D) cannot be determined</p> <p>18. The minimum width of n-diffusion and p-diffusion layer should be :</p> <p>(A) 3λ</p> <p>(B) 2λ</p> <p>(C) λ</p> <p>(D) 4λ</p> |
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| <p>19. Where are the silicon wafers placed in the reaction chamber for the epitaxial growth process ?</p> <p>(A) Cup</p> <p>(B) Boats</p> <p>(C) Ingots</p> <p>(D) Crucible</p> <p>20. As per lambda design rules for wires minimum width of metal 2 is and separation between two metal 2 layers</p> <p>(A) 4 lambda, 4 lambda</p> <p>(B) 3 lambda, 3 lambda</p> <p>(C) 4 lambda, 3 lambda</p> <p>(D) 3 lambda, 4 lambda</p> <p>21. In an AC circuit, the power factor is the cosine of the :</p> <p>(A) Voltage angle</p> <p>(B) Current angle</p> <p>(C) Phase angle</p> <p>(D) Resistance angle</p> | <p>22. In a parallel resonant circuit, the impedance is minimum at :</p> <p>(A) Resonant frequency</p> <p>(B) Half the resonant frequency</p> <p>(C) Double the resonant frequency</p> <p>(D) Quarter the resonant frequency</p> <p>23. Norton's equivalent current source is connected in :</p> <p>(A) Series with the load</p> <p>(B) Parallel with the load</p> <p>(C) Series with the source</p> <p>(D) Parallel with the source</p> <p>24. The unit step response of a first-order system exhibits :</p> <p>(A) Oscillations</p> <p>(B) Exponential rise</p> <p>(C) Exponential decay</p> <p>(D) Sine wave behaviour</p> |
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| <p>25. In network synthesis, what is the role of the Hurwitz criterion ?</p> <p>(A) To determine the stability of a control system</p> <p>(B) To check the realizability of a network</p> <p>(C) To analyze the frequency response of a circuit</p> <p>(D) To evaluate the accuracy of numerical methods in solving circuits</p> | <p>27. What is the purpose of the Bode plot in network analysis ?</p> <p>(A) To represent the step response of a system</p> <p>(B) To visualize the frequency response of a system</p> <p>(C) To analyze the transient behavior of a circuit</p> <p>(D) To plot the time domain behavior of a network</p> |
| <p>26. The Smith chart is primarily used for the analysis of :</p> <p>(A) Transmission lines</p> <p>(B) Filters</p> <p>(C) Log Amplifiers</p> <p>(D) Oscillators</p> | <p>28. What is the purpose of the Smith predictor in control system design ?</p> <p>(A) To compensate for time delays in the system</p> <p>(B) To enhance the transient response of the system</p> <p>(C) To reduce overshoot in the step response</p> <p>(D) To improve the frequency response of the system</p> |

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| <p>29. The concept of “duality” in network synthesis refers to :</p> <p>(A) The relationship between input and output signals in a system</p> <p>(B) The interchangeability of resistors and capacitors in a network</p> <p>(C) The dual representation of a network in terms of nodes and branches</p> <p>(D) The ability of a network to exhibit both series and parallel elements</p> <p>30. Consider the signal $x[n] = 2^n u[n]$ having Z-transform as $X(z)$ with ROC R. The inverse Z transform for $X(2z)$ will be :</p> <p>(A) $u[n]$</p> <p>(B) $2^n u[n]$</p> <p>(C) $4^n u[n]$</p> <p>(D) $2^{2n} u[n]$</p> | <p>31. What does the h_{FE} value represent in the common-emitter configuration ?</p> <p>(A) Reverse current transfer ratio</p> <p>(B) Output impedance</p> <p>(C) Voltage gain</p> <p>(D) Forward current transfer ratio</p> <p>32. How does the Schmitt trigger eliminate the erratic operation in the op-amp comparator ?</p> <p>(A) By reducing the sensitivity</p> <p>(B) By introducing negative feedback</p> <p>(C) By using positive feedback</p> <p>(D) By increasing the reference voltage</p> <p>33. What is the main disadvantage of half-wave rectifier compared to full wave rectifier ?</p> <p>(A) Higher cost</p> <p>(B) Lower efficiency</p> <p>(C) Limited voltage output</p> <p>(D) More complex circuitry</p> |
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| <p>34. In any circuit connections, what is the biasing direction for the emitter and collector of a transistor ?</p> <p>(A) Both are forward biased</p> <p>(B) Both are reverse biased</p> <p>(C) Emitter is forward, and collector is reverse biased</p> <p>(D) Emitter is reverse, and collector is forward biased</p> <p>35. In a CE configuration, an emitter resistor is used for</p> <p>(A) Stabilization</p> <p>(B) AC signal bypass</p> <p>(C) Collector bias</p> <p>(D) Higher gain</p> | <p>36. Explain the concept of phase noise in the context of PLLs :</p> <p>(A) Phase noise is the phase deviation between the reference and feedback signals</p> <p>(B) Phase noise is the undesirable random fluctuations in the phase of the output signal</p> <p>(C) Phase noise is the intentional modulation of the VCO frequency</p> <p>(D) Phase noise is the difference in phase between the input and output of the phase comparator</p> <p>37. Oscillators operate on the principles of</p> <p>(A) Signal feedthrough</p> <p>(B) Positive feedback</p> <p>(C) Negative feedback</p> <p>(D) Ultra high gain</p> <p>38. Following circuits can be used as voltage to frequency converter :</p> <p>(A) Astable multivibrator</p> <p>(B) Crystal oscillator</p> <p>(C) Schmitt trigger</p> <p>(D) CE amplifier</p> |
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| <p>39. What is the advantage of using IC LM725 ?</p> <p>(A) Very high gain</p> <p>(B) Very high BW</p> <p>(C) High CMRR</p> <p>(D) High slew rate</p> <p>40. Which of the following units is used to convert square wave into triangular wave ?</p> <p>(A) Differentiator</p> <p>(B) Integrator</p> <p>(C) Schmitt trigger</p> <p>(D) Monoshot multivibrator</p> <p>41. How is the complement nature of NOR represented in its graphic symbol ?</p> <p>(A) A small square</p> <p>(B) A small triangle</p> <p>(C) A small circle</p> <p>(D) A small diamond</p> | <p>42. What does SPLD stand for in the context described ?</p> <p>(A) Simple Programming Logic Device</p> <p>(B) Sequential Programmable Logic Device</p> <p>(C) Systematic PLD</p> <p>(D) Structured Programming and Logic Device</p> <p>43. Which statement accurately describes the programming of an FPGA ?</p> <p>(A) FPGA programming is permanent and cannot be changed.</p> <p>(B) FPGA programming is done using machine code</p> <p>(C) FPGA programming is done using hardware description languages (HDLs) or schematic entry</p> <p>(D) FPGA programming is only possible in a controlled laboratory environment</p> |
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| <p>44. What is the role of the Global Clock Network in CPLD ?</p> <p>(A) It synchronizes all clock signal in the devices</p> <p>(B) It is responsible for configuring the entire devices</p> <p>(C) It has no impact on timing constraints</p> <p>(D) It is used for power consumption</p> <p>45. For design of 16 : 1 MUX how many select lines are required ?</p> <p>(A) 1</p> <p>(B) 2</p> <p>(C) 3</p> <p>(D) 4</p> <p>46. A decoder converts 'n' inputs to outputs.</p> <p>(A) n</p> <p>(B) n^2</p> <p>(C) 2^n</p> <p>(D) n^n</p> | <p>47. The expression for number of modulo of a ripple counter with 'n' flip-flop is :</p> <p>(A) 2^n</p> <p>(B) n</p> <p>(C) $2^n - 1$</p> <p>(D) n^2</p> <p>48. Output values of Moore type FSM are determined by its</p> <p>(A) Clock input</p> <p>(B) Current state</p> <p>(C) Input values</p> <p>(D) Output values</p> <p>49. VHDL represents a component's external behavior and interface.</p> <p>(A) Signal</p> <p>(B) Port</p> <p>(C) Entity</p> <p>(D) Architecture</p> |
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| <p>50. Race Around condition can be avoided in digital logic circuits using :</p> <p>(A) Shift register</p> <p>(B) Full adder</p> <p>(C) AND gate</p> <p>(D) Master slave JK flip-flop</p> <p>51. The configuration in which each LED receives operating current of 8 mA from power supply while the port lines sink the current on each port line is :</p> <p>(A) driver configuration</p> <p>(B) common anode configuration</p> <p>(C) common cathode configuration</p> <p>(D) buffer configuration</p> <p>52. If no key is pressed microcontroller 8051 port will read :</p> <p>(A) 0</p> <p>(B) 1</p> <p>(C) NC</p> <p>(D) 7</p> | <p>53. What is the status of the carry, auxiliary carry and parity flags after executing following instructions ?</p> <p>MOV A,#9C</p> <p>ADD A,#64H</p> <p>(A) CY = 0, AC = 0, P = 0</p> <p>(B) CY = 1, AC = 1, P = 0</p> <p>(C) CY = 0, AC = 1, P = 0</p> <p>(D) CY = 1, AC = 1, P = 1</p> <p>54. What is the frequency of the clock that is being used as the clock source for the timer ?</p> <p>(A) some externally applied frequency f</p> <p>(B) controller's crystal frequency f</p> <p>(C) controller's crystal frequency/12</p> <p>(D) externally applied frequency/12</p> <p>55. In 8086 microprocessor, the address bus is bit wide.</p> <p>(A) 12 bit</p> <p>(B) 10 bit</p> <p>(C) 16 bit</p> <p>(D) 20 bit</p> |
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| <p>56. The register AX is formed by grouping</p> <p>(A) BH & BL</p> <p>(B) CH & CL</p> <p>(C) AH & AL</p> <p>(D) DH & DL</p> <p>57. A 20-bit address bus allows access to a memory of capacity :</p> <p>(A) 1 MB</p> <p>(B) 2 MB</p> <p>(C) 4 MB</p> <p>(D) 8 MB</p> <p>58. What is the Address (SFR) for TCON, SCON, SBUF, PCON and PSW respectively ?</p> <p>(A) 88H, 98H, 99H, 87H, 0D0H</p> <p>(B) 98H, 99H, 87H, 88H, 0D0H</p> <p>(C) 0D0H, 87H, 88H, 99H, 98H</p> <p>(D) 87H, 88H, 0D0H, 98H, 99H</p> <p>59. In 8051 which interrupt has highest priority ?</p> <p>(A) IE1</p> <p>(B) TF0</p> <p>(C) IE0</p> <p>(D) TF1</p> | <p>60. Which of the following should a microcontroller at least consist of :</p> <p>(A) CPU, ROM, I/O ports and Timers</p> <p>(B) RAM, ROM, I/O ports and Timers</p> <p>(C) CPU, RAM, I/O ports and Timers</p> <p>(D) CPU, RAM, ROM, I/O ports and Timers</p> <p>61. A non-conducting sphere has uniform charge density in it. The electric field at a point inside the sphere will be</p> <p>(A) Zero</p> <p>(B) Only due to the charge inside that point</p> <p>(C) Only due to the charge outside that point</p> <p>(D) Due to the entire charge of the sphere</p> |
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| <p>62. What happens to the current in a coil while accelerating a magnet inside it ?</p> <p>(A) Increases</p> <p>(B) Decreases</p> <p>(C) Remains constant</p> <p>(D) Reverses</p> <p>63. In an electromagnetic wave, the electric field of amplitude 6.2 V/m oscillates with a frequency of 2.4×10^{10} Hz. Estimate energy density of the wave :</p> <p>($\epsilon = 8.85 \times 10^{-12} \text{ C}^2 / \text{Nm}^2$)</p> <p>(A) $1.4 \times 10^{-10} \text{ J/m}^3$</p> <p>(B) $2.4 \times 10^{-10} \text{ J/m}^3$</p> <p>(C) $3.4 \times 10^{-10} \text{ J/m}^3$</p> <p>(D) $4.4 \times 10^{-10} \text{ J/m}^3$</p> <p>64. The ratio of conduction to displacement current density is referred to as :</p> <p>(A) Attenuation constant</p> <p>(B) Propagation constant</p> <p>(C) Loss tangent</p> <p>(D) Dielectric constant</p> | <p>65. Which component of the electric field intensity is always continuous at the boundary ?</p> <p>(A) Tangential</p> <p>(B) Normal</p> <p>(C) Horizontal</p> <p>(D) Vertical</p> <p>66. Calculate the skin depth of a material with attenuation constant of 2 units.</p> <p>(A) 2</p> <p>(B) 1</p> <p>(C) 0.5</p> <p>(D) 4</p> <p>67. The expression for velocity of a wave in the conductor is :</p> <p>(A) $V = \sqrt{(2\omega / \mu\sigma)}$</p> <p>(B) $V = \sqrt{(2\omega\mu\sigma)}$</p> <p>(C) $V = (2\omega / \mu\sigma)$</p> <p>(D) $V = (2\omega\mu\sigma)$</p> |
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| <p>68. The standing wave ratio of short circuited and open circuited lines will be :</p> <p>(A) 0</p> <p>(B) 1</p> <p>(C) -1</p> <p>(D) ∞</p> <p>69. In a good conductor the phase relation between the tangential components of electric E and the magnetic field H is as follows :</p> <p>(A) E and H are in phase</p> <p>(B) E and H are out of phase</p> <p>(C) H leads E by 90</p> <p>(D) E leads H by 45</p> <p>70. Consider a transmission line of characteristic impedance 50 ohm. Let it be terminated at one end by + j50 ohm. The VSWR produced by it in the transmission line will be :</p> <p>(A) 1</p> <p>(B) 0</p> <p>(C) Infinity</p> <p>(D) + j</p> | <p>71. The phase shift keying is a modulation technique used for transmitting :</p> <p>(A) digital data over analog channel</p> <p>(B) analog data over digital channel</p> <p>(C) binary signals only</p> <p>(D) FM signals</p> <p>72. A PLL maintains lock by comparing :</p> <p>(A) The phase of two signals</p> <p>(B) The frequency of two signals</p> <p>(C) The amplitude of two signals</p> <p>(D) The amplitude of input signal with a fixed threshold</p> <p>73. To increase level of very weak radio signals from an antenna, you would use :</p> <p>(A) an RF oscillator</p> <p>(B) an audio oscillator</p> <p>(C) an RF amplifier</p> <p>(D) an audio amplifier</p> |
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| <p>74. Which is the true statement about frequency deviation in frequency modulation ?</p> <p>(A) frequency deviation is proportional to carrier signal frequency</p> <p>(B) frequency deviation is proportional to amplitude of carrier signal</p> <p>(C) frequency deviation is proportional to modulating frequency</p> <p>(D) frequency deviation is proportional to amplitude of modulating signal</p> <p>75. The equivalent noise temperature of a network given the noise figure of the network or system is :</p> <p>(A) $T_0 (F - 1)$</p> <p>(B) $T_0 (F + 1)$</p> <p>(C) $T_0 (F)$</p> <p>(D) T_0/F</p> | <p>76. Which is called as on-off keying ?</p> <p>(A) Frequency shift keying</p> <p>(B) Uni-polar PAM</p> <p>(C) Amplitude shift keying and Uni-polar PAM</p> <p>(D) Phase shift keying</p> <p>77. Which of the following technology distributes the coverage of the cell and extends the cell boundary to hard-to-reach places ?</p> <p>(A) Sectoring</p> <p>(B) Cell splitting</p> <p>(C) Micro cell zone concept</p> <p>(D) Scattering</p> <p>78. A planar LED is fabricated from GaAs is having a optical power emitted, which is 0.018% of optical power generated internally which is 0.6 W. Determine external power efficiency :</p> <p>(A) 0.18%</p> <p>(B) 0.32%</p> <p>(C) 0.65%</p> <p>(D) 0.9%</p> |
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| <p>79. A technique used for determining the total fiber attenuation per unit length is method.</p> <p>(A) Frank</p> <p>(B) Cut-off</p> <p>(C) Cut-back</p> <p>(D) OSA</p> | <p>81. The turn-off time of thyristor is 30 m sec at 50°C. Its turn-off time at 100° is :</p> <p>(A) same</p> <p>(B) 15 m sec</p> <p>(C) 60 m sec</p> <p>(D) 100 m sec</p> |
| <p>80. Global Sensor Network is built for :</p> <p>(A) Reducing cost and time for development</p> <p>(B) Reducing cost and increasing time for development</p> <p>(C) Increasing cost and increasing time for development</p> <p>(D) Increasing cost and decreasing time for development</p> | <p>82. A six pulse thyristor rectifier bridge is connected to a balanced 50 Hz three-phase ac source. Assuming that the dc output current of the rectifier is constant, the lowest harmonic component in the ac source line current is :</p> <p>(A) 100 Hz</p> <p>(B) 150 Hz</p> <p>(C) 250 Hz</p> <p>(D) 300 Hz</p> |

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| <p>83. In a thyristor dc chopper, which type of commutation results in best performance ?</p> <p>(A) current commutation</p> <p>(B) load commutation</p> <p>(C) voltage commutation</p> <p>(D) supply commutation</p> <p>84. Consider the following statements :
The diodes in a voltage source inverter (McMurray Inverter) should be able to :</p> <p>(1) Withstand a large voltage in the reverse direction</p> <p>(2) Carry the commutating current excess of load current</p> <p>(3) Provide the required reverse-bias to the outgoing thyristor</p> <p>(4) Feedback the reactive current to the source.</p> <p>Of these statements :</p> <p>(A) (1), (2) and (3) are correct</p> <p>(B) (1), (3) and (4) are correct</p> <p>(C) (2), (3) and (4) are correct</p> <p>(D) (1), (2) and (4) are correct</p> | <p>85. In a dc motor, if the field coils get opened, the speed of the motor will :</p> <p>(A) decrease</p> <p>(B) come to a stop</p> <p>(C) increase</p> <p>(D) become zero</p> <p>86. Voltage induced in the rotor of the induction motor when it runs at synchronous speed is :</p> <p>(A) very near input voltage to stator</p> <p>(B) slip time the input voltage</p> <p>(C) zero</p> <p>(D) very near input voltage to rotor</p> <p>87. Which of the following is an open loop control system ?</p> <p>(A) Field controlled D.C. motor</p> <p>(B) Wardleopard control</p> <p>(C) Metadyne</p> <p>(D) Stroboscope</p> |
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88. Any externally introduced signal affecting the controlled output is called a :

- (A) feedback
- (B) stimulus
- (C) signal
- (D) gain control

89. For a type one system, the steady-state error due to step input is equal to :

- (A) Infinite
- (B) Zero
- (C) 0.25
- (D) 0.5

90. In a control system the output of the controller is given to :

- (A) final control element
- (B) amplifier
- (C) comparator
- (D) sensor

91. Match List I with List II :

List I

- (a) Sensors
- (b) Networks
- (c) Augmented intelligence
- (d) Standards

List II

- (i) Analytical tools that improve the ability to describe phenomenon
- (ii) Commonly accepted prescriptions for action
- (iii) A device that generates an electronic signal from a physical condition
- (iv) A mechanism for communicating an electronic signal

Choose the correct answer from the options given below :

- (a) (b) (c) (d)
- (A) (ii) (i) (iii) (iv)
- (B) (iii) (iv) (ii) (i)
- (C) (iii) (iv) (i) (ii)
- (D) (ii) (i) (iv) (iii)

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| <p>92. Semiconductor strain gauges depend upon for their action.</p> <p>(A) Piezoelectric effect</p> <p>(B) Piezo-resistive effect</p> <p>(C) Ferro-magnetic</p> <p>(D) Superconductivity</p> <p>93. On applying mechanical stresses, a material gets electrically polarized. It must be a :</p> <p>(A) Superconducting material</p> <p>(B) Piezoelectric material</p> <p>(C) Ferro-magnetic material</p> <p>(D) Ferroelectric material</p> <p>94. of a measuring system refers to its ability to follow instant by instant the measured with time.</p> <p>(A) Bandwidth</p> <p>(B) Fidelity</p> <p>(C) Measurement lag</p> <p>(D) Settling time</p> | <p>95. What will be the ratio of amplitudes of largest (maximum) signal to smallest (minimum) signal to which the system is subjected ?</p> <p>(A) Time constant</p> <p>(B) Settling period</p> <p>(C) Dynamic range</p> <p>(D) Bandwidth</p> <p>96. The sweep generator of a CRO is used to produce :</p> <p>(A) sinusoidal voltage for the horizontal deflection of electron beam</p> <p>(B) sawtooth voltage for the vertical deflection of electron beam</p> <p>(C) sinusoidal voltage for the vertical deflection of electron beam</p> <p>(D) sawtooth voltage for the horizontal deflection of electron beam</p> |
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| <p>97. Digital voltmeters can be used to measure</p> <p>(A) voltage only</p> <p>(B) voltage, temperature. pressure etc.</p> <p>(C) voltage and current</p> <p>(D) voltage and resistance</p> <p>98. What is the relation between scale factor and sensitivity of a transducer ?</p> <p>(A) Scale factor is double of sensitivity</p> <p>(B) Scale factor is inverse of sensitivity</p> <p>(C) Sensitivity is inverse of scale factor</p> <p>(D) Sensitivity is equal to scale factor</p> | <p>99. Needle electrode is used to measure</p> <p>(A) EKG</p> <p>(B) EEG</p> <p>(C) EOG</p> <p>(D) EMG</p> <p>100. Which rhythm is the principal component of the EEG that indicates the alertness of the brain ?</p> <p>(A) Theta rhythm</p> <p>(B) Gamma rhythm</p> <p>(C) Beta rhythm</p> <p>(D) Alpha rhythm</p> |
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